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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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|----------|-------------|-------|----------|------|---------------|
| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
| 10007836 | 11/05/2001 | 712 | | 2154 | FOLLANSBEE, J |

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W. Ley, Pw, J.

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/605,260 06/30/2000 PAT 6,327,647

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** FOREIGN APPLICATIONS VERIFIED:

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|---|---|--|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed 35 USC 119 conditions met | | <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no |
| Verified and Acknowledged Examiner's initials | | ATTORNEY DOCKET NO SC90674A CD1 |
| TITLE : Method and apparatus for interfacing a processor to a coprocessor | | |
| U.S. DEPT. OF COMM./PAT & TM-PTO-435L (Rev. 12-94) | | |

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|--|-----------|---|---------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs.Drwg. |
| | | Print Fig. | |
| TERMINAL DISCLAIMER | | Primary Examiner | |
| | | PREPARED FOR ISSUE Application Examiner | |
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